

*# 2/EP, S  
N. B. et al  
9-19-01*

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**



**In re Application of:**

Farnworth et al.

**Serial No.:** Not yet assigned

**Filed:** August 6, 2001

**For:** HERMETIC CHIP AND METHOD OF  
MANUFACTURE

**Examiner:** Unknown

**Group Art Unit:** Unknown

**Attorney Docket No.:** 2741.5US (95-0804.4)

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**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
Washington, D.C. 20231

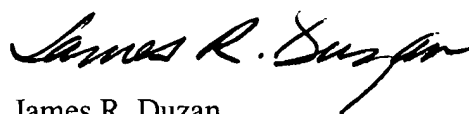
Sir:

The present application is a continuation of application Serial No. 09/518,293, filed March 3, 2000, pending, which is a continuation of application Serial No. 09/244,733, filed February 5, 1999, now U.S. Patent 6,084,288, issued July 4, 2000, which is continuation of application Serial No. 08/910,613, filed August 13, 1997, now U.S. Patent 5,903,044, issued May 11, 1999, which is a continuation of application Serial No. 08/614,178, filed March 12, 1996, now U.S. Patent 5,682,065, issued October 28, 1997.

Pursuant to M.P.E.P. 2001.06(b), the Examiner is respectfully requested to consider the information of record in the prior application, and to confirm in the first Office Action on the merits that such art has in fact been reviewed. A PTO-1449 form listing all of the information of record in the prior application is enclosed herewith.

This Information Disclosure Statement is filed within three (3) months of the filing date of the above-identified application, and no certification pursuant to 37 C.F.R. § 1.97(c) or a fee pursuant to 37 C.F.R. 1.17(p) is required.

Respectfully submitted,



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Date: August 6, 2001

JRD/sls:djp

Enclosure: Form PTO-1449